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SEP 0 4 2001
PATENT & TRADEMARK OFFICE

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT	ATTY. DOCKET NO. 11157-23	SERIAL NO. 09/854,905
(Use several sheets if necessary)	APPLICANT BRIAN MOORE	
	FILING DATE May 15, 2001	GROUP 2858

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPLICABLE
JN		3,689,885	09/05/72	Kaplan et al.	340	152T	09/15/70
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	DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
	0098399	06/04/86	Europe	~~G 01 R 31~~	~~28~~	Not needed
	0911640	04/28/99	Europe	~~G 01R 29~~	~~027~~	Not needed
	99/16107	04/01/99	PCT	~~H01L~~	~~-~~	Not needed
	99/32893	07/01/99	PCT	~~G01R 31~~	~~00~~	Not needed
ON	99/65287	12/23/99	PCT	~~N/a~~	~~N/a~~	Not needed

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ON Introduction to Component Testing – by Anthony K. Stevens – 1986 – pages 3-14 & pages 91-119

ON Semiconductor Material and Device Characterization – by Dieter K. Schroder – 1990 – pages 1-40

ON CMOS Digital Integrated Circuits – by Sung-Mo (Steve) Kang & Yusuf Leblebici – 1999 – pages 220-242, Chaps 6, 9, 11, 15

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ON Testing the Monster Chip – IEEE Spectrum, (July 1999) – by Yervant Zorian – Pages 54-60

EXAMINER: Jimmy Nguyen DATE CONSIDERED 8/26/04

*EXAMINER: Initial if citation not considered, whether or not citation is in conformance with MPEP 609; draw Line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

PTO/SB/08A (10-01)
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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 1

Complet if Known	
Application Number	09/854,905
Filing Date	May 15, 2001
First Named Inventor	MOORE, Brian
Group Art Unit	2829
Examiner Name	N/A
Attorney Docket Number	11157-23

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
JW		K. ARABI et al. "Digital Oscillation-Test Method for Delay and Stuck-at-Fault Testing of Digital Circuits", International Test Conference, 1998, pp. 91-98.	

Examiner Signature	Jimmy Nguyen	Date Considered	6/4/03

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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